

Re: [PATCH] Maintaining turnstile aligned to 128 bytes in i386 CPUs

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- *From:* John Baldwin <jhb@xxxxxxxxxxx>
  - *Date:* Tue, 16 Jan 2007 18:06:26 -0500
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On Tuesday 16 January 2007 17:54, Attilio Rao wrote:

2007/1/16, Ivan Voras <ivoras@xxxxxx>:

Kip Macy wrote:

x86 pre-P4 had 32-byte cache lines. Thus older processors will not

benefit.

But it does seem to hurt the performance a bit – maybe it's time to add another CPU option like I586\_CPU and I686\_CPU?

Well, it is my feeling that probably the align\_cache parameter should be a run-time settled parameter (in particular for ia32 CPUs which changed a lot along the years).

Yes. I think UMA\_ALIGN\_CACHE should be a magic cookie value and that the MD code should provide a cache line size to uma(4) during bootup, and I think you can probably just axe UMA\_ALIGN\_SYNC and use UMA\_ALIGN\_CACHE for the turnstile and sleep queue zones.

—  
John Baldwin

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