

freebsd-hackers: Re: BUS DMA sync

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From: M. Warner Losh (*imp_at_bsdimp.com*)

Date: 09/26/03

Date: Thu, 25 Sep 2003 19:47:12 -0600 (MDT)

To: vjardin@free.fr

In message: <200309250014.53816.vjardin@free.fr>

Vincent Jardin <vjardin@free.fr> writes:

: What's about PREREAD ? What kind of CPU synchronization is required prior a

: DMA read ? There is no cache during a device to host memory process, isn't it

: ?

There is on MIPS.

Warner

freebsd-hackers@freebsd.org mailing list

<http://lists.freebsd.org/mailman/listinfo/freebsd-hackers>

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