

## Re: Weird PCI interrupt delivery problem (resolution, sort of)

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*Source:* <http://unix.derkeiler.com/Mailing-Lists/FreeBSD/hackers/2006-01/msg00238.html>

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- *From:* Craig Boston <[craig@xxxxxxxxxxxxxxx](mailto:craig@xxxxxxxxxxxxxxx)>
  - *Date:* Fri, 20 Jan 2006 09:27:31 -0600
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On Thu, Jan 19, 2006 at 10:17:39PM -0700, Scott Long wrote:  
> This points to a bus coherency problem. I wonder if your BIOS is  
> incorrectly setting the memory region of the apics as cachable. You'll  
> want to bug Baldwin about this.

I CC-ed him on my post since he was working with me on the problem before. For some reason the Cc: header got wiped out when it went to the list (but I checked my server logs and it did deliver a copy of the message to him).

Craig

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freebsd-hackers@xxxxxxxxxxxxx mailing list

<http://lists.freebsd.org/mailman/listinfo/freebsd-hackers>

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- *Follow-Ups:*

- ◆ [\*\*Re: Weird PCI interrupt delivery problem \(resolution, sort of\)\*\*](#)  
    ◇ *From:* John Baldwin

- *References:*

- ◆ [\*\*Weird PCI interrupt delivery problem \(resolution, sort of\)\*\*](#)  
    ◇ *From:* Craig Boston
- ◆ [\*\*Re: Weird PCI interrupt delivery problem \(resolution, sort of\)\*\*](#)  
    ◇ *From:* Scott Long

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