

Re: Dual-core CPU vs. very large cache

Source: <http://unix.derkeiler.com/Mailing-Lists/FreeBSD/performance/2006-04/msg00047.html>

- *From:* "David O'Brien" <obrien@xxxxxxxxxxx>
 - *Date:* Thu, 27 Apr 2006 12:03:13 -0700
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On Thu, Apr 27, 2006 at 12:11:05PM +0100, Steven Hartland wrote:

----- Original Message -----

From: "Mike Jakubik" <mikej@xxxxxxxxxxx>

No! Socket AM2 is the DDR2 939-pin Athlon64 desktop replacement.
Socket F(1207) is DDR2 the 940-pin Opteron server replacement.

Same crap, different pins. The change simply allows AMD cpus to use DDR2 memory, nothing more.

Getting off topic now but I'd submit to you that a 1207 pin vs 940 pin is setting up for the access requirements of quad core something that AM2 is not going to be capable of hence quite different indeed.

Nope. Quad core is internal connections between cores – not external.
So you don't need extra pins to support quad-core vs. dual-core.

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-- David (obrien@xxxxxxxxxxx)

Q: Because it reverses the logical flow of conversation.

A: Why is top-posting (putting a reply at the top of the message) frowned upon?

freebsd-performance@xxxxxxxxxxx mailing list

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